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TO: USPTO

ZILKA-KOTAB, PC

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FAX COVER SHEET

Date: December 17, 2007	Phone Number	Fax Number
To: USPTO: Commissioner for Patents	(571) 273-8300	
From: Kevin J. Zilka		

Docket No.: RZMI-P0101-US

App. No: 10/682,579

Total Number of Pages Being Transmitted, Including Cover Sheet: 08

Message:

Please deliver to the Commissioner for Patents.

Thank you,

Kevin J. Zilka

☐ Original to follow Via Regular Mail ☒ Original will Not be Sent ☐ Original will follow Via Overnight Courier

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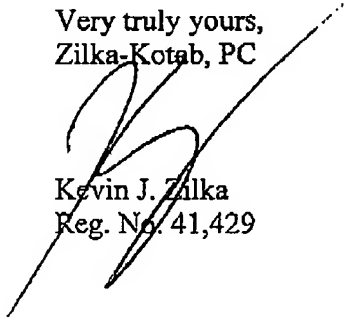
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Docket No. RZMI-P0101-US
ADVANCED TELECOMMUNICATIONS PROCESSOR
US Application 10/682,579
Filed: 10/08/2003
Owner of Record: Raza Microelectronics, Inc.

Dear Sir:

Please enter the enclosed Statement under 37 CFR 3.73(b) and Revocation and Power of Attorney into the file of the referenced application.

Very truly yours,
Zilka-Kotab, PC


Kevin J. Zilka
Reg. No. 41,429

Enclosures

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being facsimile transmitted to the Commissioner for Patents at facsimile number: (571) 273-8300 on the date below.

Dana Chan
Dana Chan

12/17/07
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PTO/SB/98 (12-07)

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Hass et al.Application No./Patent No.: 10/682,579 Filed/Issue Date: 10/08/2003Entitled: **ADVANCED TELECOMMUNICATIONS PROCESSOR**Raza Microelectronics, Inc. a corporation
(Name of Assignee) (Type of Assignee: e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest
(The extent (by percentage) of its ownership interest is _____ %)

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel 014598, Frame 0038, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.
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☐ Additional documents in the chain of title are listed on a supplemental sheet.

- ☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

L. William Caraccio

Signature

L. William Caraccio

Printed or Typed Name

V.P. & General Counsel

Title

12-13-07

Date

(408) 434-5510

Telephone Number

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**ADVANCED PROCESSOR TRANSLATION LOOKASIDE BUFFER MANAGEMENT IN A
MULTITHREADED SYSTEM**

US Application 11/704,709

Filed: 02/08/2007

Owner of Record: Raza Microelectronics, Inc.

ADVANCED TELECOMMUNICATIONS PROCESSOR

US Application 60/490,236

Filed: 07/25/2003

Owner of Record: Raza Microelectronics, Inc.

ADVANCED TELECOMMUNICATIONS PROCESSOR

US Application 60/416,838

Filed: 10/08/2002

Owner of Record: Raza Microelectronics, Inc.

ADVANCED TELECOMMUNICATIONS PROCESSOR

US Application 10/682,579

Filed: 10/08/2003

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH SYSTEM ON A CHIP INTERCONNECT TECHNOLOGY

US Application 10/898,008

Filed: 07/23/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR SCHEDULING IN A MULTITHREADED SYSTEM

US Application 10/898,007

Filed: 07/23/2004

Owner of Record: Raza Microelectronics, Inc.

**ADVANCED PROCESSOR TRANSLATION LOOKASIDE BUFFER MANAGEMENT IN A
MULTITHREADED SYSTEM**

US Application 10/898,150

Filed: 07/23/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH CACHE COHERENCY

US Application 10/897,577

Filed: 07/23/2004

Owner of Record: Raza Microelectronics, Inc.

MULTI-CORE MULTI-THREADED PROCESSING SYSTEMS WITH INSTRUCTION REORDERING IN AN IN-ORDER PIPELINE

US Application 10/930,938

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH MECHANISM FOR FAST PACKET QUEUING OPERATIONS

US Application 10/930,455

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH INTERRUPT DELIVERY MECHANISM FOR MULTI-THREADED MULTI-CPU SYSTEM ON A CHIP

US Application 10/931,003

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH MECHANISM FOR MAXIMIZING RESOURCE USAGE IN AN IN-ORDER PIPELINE WITH MULTIPLE THREADS

US Application 10/930,939

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH SCHEME FOR OPTIMAL PACKET FLOW IN A MULTI-PROCESSOR SYSTEM ON A CHIP

US Application 10/930,186

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH INTERFACING MESSAGING NETWORK TO A CPU

US Application 10/930,937

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

Page 3 of 5

ADVANCED PROCESSOR WITH MECHANISM FOR PACKET DISTRIBUTION AT HIGH LINE RATE

US Application 10/931,014

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH NOVEL LEVEL 2 CACHE DESIGN

US Application 10/897,576

Filed: 07/23/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH USE OF BRIDGES ON A DATA MOVEMENT RING FOR OPTIMAL REDIRECTION OF MEMORY AND I/O TRAFFIC

US Application 10/930,179

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH IMPLEMENTATION OF MEMORY ORDERING ON A RING BASED DATA MOVEMENT NETWORK

US Application 10/930,187

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH MECHANISM FOR ENFORCING ORDERING BETWEEN INFORMATION SENT ON TWO INDEPENDENT NETWORKS

US Application 10/930,456

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

ADVANCED PROCESSOR WITH A THREAD AWARE RETURN ADDRESS STACK OPTIMALLY USED ACROSS ACTIVE THREADS

US Application 10/930,175

Filed: 08/31/2004

Owner of Record: Raza Microelectronics, Inc.

HIGH PERFORMANCE INTEGRATED CIRCUIT WITH LOW SKEW CLOCKING NETWORKS AND IMPROVED LOW POWER OPERATING MODE HAVING REDUCED RECOVERY TIME

US Application 60/630,527

Filed: 11/22/2004

Owner of Record: Raza Microelectronics, Inc.

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HIGH PERFORMANCE INTEGRATED CIRCUIT WITH LOW SKEW CLOCKING NETWORKS
AND IMPROVED LOW POWER OPERATING MODE HAVING REDUCED RECOVERY TIME
US Application 11/283,154
Filed: 11/18/2005
Owner of Record: Raza Microelectronics, Inc.

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REVOCATION OF PRIOR POWERS OF ATTORNEY
AND GRANT OF NEW POWER OF ATTORNEY

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

As assignee of record of the entire interest of the above-identified patent applications(s) and/or patent(s), all powers of attorney previously given are hereby revoked and the following agents are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office connected therewith.

Kevin J. Zilka (Reg. No. 41,429); and
Dominic M. Kotab (Reg. No. 42,762).

Please send all correspondence for these applications(s) and/or patent(s) as follows:

Zilka-Kotab, PC
P.O. Box 721120
San Jose, California 95172-1120

Please direct any calls to Kevin Zilka at (408) 505-5100.

Date

12-13-07



L. William Caraccio
V.P. & General Counsel
Raza Microelectronics, Inc.